

Please complete and submit this ATLAS Standard Form to ATLAS RHA Coordinator ([ARC](#)), at least 2 weeks after the date of the test.

1. General information:

1.1	Date of the test:	05/17/2003
1.2	Pre-selection, or Qualification ? (specify)	Qualification
1.3	Name of the ATLAS (or other) System:	MUON CSC
1.4	Name of the board in the System:	ASM 1
1.5	Person responsible for the test:	Anand Kandasmy
1.6	Institute:	Brookhaven National Laboratory
1.7	Email:	anand@bnl.gov
1.8	Person responsible for RHA of the Board:	Anand Kandasmy
1.9	Institute:	Brookhaven National Laboratory
1.10	Email:	anand@bnl.gov

2. Component:

2.1	Name:	IC71 (ASM1 CSC Preamp/Shaper)
2.2	Part Number:	IC71
2.3	Type (see section 13.1):	Linear Device
2.4	Function (see section 13.1):	Charge amplification and Shaping
2.5	Main specification of the component:	
2.6	Design (specify: COTS/ASIC):	CMOS ASIC
2.7	Design center (if known):	Brookhaven National Laboratory
Manufacturer:		
2.8	Name of the manufacturer:	Agilent (HP)
2.9	Address of the manufacturer (if known):	
2.10	Phone of the manufacturer (if known):	
2.11	Email of the manufacturer (if known):	
2.12	Web URL of the manufacturer (if known):	
Sampling:		
2.13	Number of tested components (irradiated):	4
2.14	Number of reference components (un-irradiated):	1
Batch origin:		
2.15	Batch origin (Homogeneous/Unknown):	Homogeneous
2.16	Manufacturing date code (for homogeneous batch):	T17E-AT(MOSIS)
2.17	Manufacturing line code (for homogeneous batch):	
Technology:		
2.18	Name of the technology (if known):	HP AMOS14TB
2.19	Technology (CMOS/BiCMOS/Bipolar/AsGa/Other):	CMOS
2.20	Minimum geometry (μm):	0.5
Package:		
2.21	Type:	QFP
2.22	Part number:	IC71
2.23	Number of pin:	100
2.24	Ceramic ? Plastic ? hybrid ? (specify)	Plastic

3. Radiation:

3.1	Name of the radiation facility:	Northeast Proton Therapy Center
3.2	Address of the radiation facility:	Massachusetts Gen. Hospital/Boston
3.3	Radiation source (see 13.2) :	Cyclotron
3.4	Radiation type (see 13.2) :	Proton Beam
3.5	Radiation energy (MeV) :	226
3.6	Minimum & maximum flux (particle per second) :	8E9 protons.cm ⁻² /sec
3.7	Total fluence after last step (1 MeV eq. n/cm ²) :	2.6E13
3.8	TID after last step (Gy) :	14000
3.9	Dosimetry / Calibration method:	Vacuumless faraday cup

4. Radiation test method (see 12.3):

4.1	ATLAS Standard NIEL Test Method?	Y
4.2	Other TID test method (specify)?	

5. Thermal and voltage stresses:

5.1	Temperature (°C) :	25
5.2	Supply voltage (specify) :	3.3V
5.3	AC operation (Y/N) ?	N
5.4	If "yes" to 5.3, which AC operation?	
5.5	If "yes" to 5.3, which frequency?	

Please select and complete one or several sections among sections 6 to 9, according to the device and function(s) you are testing:

6. If your circuit is an analog circuit or contains analog functions:

6.1	Did you search for parasitic transient if any?	N
6.2	Did you record parasitic transient if any?	N
6.3	Did you search for permanent dysfunction if any?	N
6.4	Did you record permanent dysfunction if any?	N
6.5	Did you measure current consumption?	Y
6.6	Did you record current consumption?	Y
6.7	Did you perform an automatic power cycle if a permanent dysfunction or current increase occurs?	N
6.8	Description of operation and measurements of the analog (part of) circuit: The device currents were monitored online during irradiation using a data acq. system. The test was interested in observing for any latchups that might occur.	

7. If your circuit is a digital circuit or contains digital functions:

7.1	Did you check all functions in the circuit (Y/N) ?	
7.2	If “no” to 6.13, which function did you not exercise (specify) ?	
7.3	At what frequency did you exercise the circuit?	
7.4	Did you search for transient errors (Y/N) ?	
7.5	Did you automatically record transient errors if any (Y/N) ?	
7.6	Did you search for permanent errors (Y/N) ?	
7.7	Did you automatically record permanent errors if any (Y/N) ?	
7.8	Did you perform an automatic reset after a permanent error if any (Y/N) ?	
7.9	Did you perform an automatic power cycle after a permanent error if any (Y/N) ?	
7.10	Description of operation and measurements of the digital (part of) circuit:	

8. If your circuit is a memory or a register, or contains memory(ies) or registers:

8.1	Did you check all the memories and registers of the device under test (Y/N) ?	
8.2	If “no” to 8.1, which memories or registers will you not check (specify)?	
8.3	Did you search for 0 => 1 upsets (Y/N) ?	
8.4	Did you search for 1 => 0 upsets (Y/N) ?	
8.5	Time required to write in the memory or in the register under test?	
8.6	After a “write” sequence, do you read the memory or register once, or periodically (answer by “once” or “each [time between 2 consecutive read]”) ?	
8.7	After a “write” sequence, what is the time during which the memory or the register is (once or periodically) read ?	
8.8	Did you automatically record bit errors if any (Y/N) ?	
8.9	Did you check the “write” function of the memory or register after permanent upset if any (Y/N) ?	
8.10	Did you perform an automatic reset after permanent upset if any (Y/N) ?	
8.11	Did you perform an automatic power cycle after permanent upset if any (Y/N) ?	
8.12	Description of operation and measurements of the memories and/or of the registers:	

11. Results, cont.

	11.1	11.2	11.3	11.4	11.4	11.5	11.6	11.7
	Serial number of the device under test	number of SEU	number of bits permanently stuck	number of SEL	number of destructive SEE	recovery after power cycle (Y/N)?	recovery after reset (Y/N)?	Failure mechanism (if any): for component "dead" or out of specification, give explanations and numbers
		total fluence	total fluence	total fluence	Total fluence			
6								
7								
8								
9								
10								
11								
12								
13								
14								
15								

12. Comments

Use the space below to comment test results, or to report them if the above-dedicated space is inappropriate for you.

The devices are tolerant to high energy hadrons (> 20 MeV) and did not exhibit latchups during irradiation. The circuit parameter changes due to irradiation are well within the rejection criteria set.
See attached document.

13. Guidelines

13.1 Type and Function

Type	Function
Analogue device	ADC; Analogue memory; Analogue multiplexor; DAC; LVDS driver; LVDS receiver; Modulator/Demodulator; Voltage/Frequency converter
Data transmission Component	Receiver; Transceiver; Transmitter
Front-end electronic device	Drift Time Measurement; Multiple functions; Readout memory
Linear device	Amplifier; Comparator; Operational amplifier; Voltage reference;
Memory	SRAM
Microprocessor or peripheral	Microcontroller; Microprocessor
Optoelectronic component	Laser; Light emitting diode – LED; PIN diode; VCSEL
Power device	DC-DC converter; Power transistor; Voltage regulator
Programmable device	EEPROM; FPGA; Lookup table; Programmable delay
Passive component	Capacitor
Interfaces/Communication	LVDS; Switch
Mixed A/D device	Multiple functions
Logic gates	NOR, NAND, etc.

13.2 Radiation source and type

Source of radiation	Type of radiation
Accelerator	Electron, proton, spallation neutron
Am-241	Ions (fission products)
Cf-252	Ions (fission products)
Co-60	Photon gamma 1.173 MeV and 1.332 MeV
Cs-137	Photon gamma 0.662 MeV
Cyclotron	Proton, ion (specify), spallation neutron
Reactor	Neutron
Tandem accelerator	Protons, ions
Van-de-Graaf	Electron
X-Ray generator	Photon X

13.3 Radiation test methods:

see ATLAS Policy on Radiation Tolerant Electronics rev. 2, pp. 20-26

http://atlas.web.cern.ch/Atlas/GROUPS/FRONTEND/WWW/RAD/RadWebPage/ATLASPolicy/APRTE_rev2_250800.pdf

	US ATLAS MUON SPECTROMETER CATHODE STRIP CHAMBER	
Anand Kandasamy anand@bnl.gov	SEE/SEU Test Report For CSC Front-end Electronics	7/2/2003

1. Overview:

SEE/SEU tests using high energy particles (> 20 MeV) tests were conducted on preamplifier/shaper integrated circuits for the Cathode Strip Chamber front-end electronics. This test was targeted towards the analog circuitry that performs charge amplification. The analog charge amplification and shaping circuit identified as IC71 is a custom ASIC developed at Brookhaven National Laboratory and fabricated in 0.5 μm CMOS technology

2. Hadron fluence Levels in CSC.

Worst case Simulated Radiation Levels (SRL) and Radiation Tolerance Criteria (RTC) for the CSC is given below. This data is obtained from the ATLAS Radiation Tolerance Criteria documents and extraction tools.

Qualification/Pre-selection:

Zmin (cm)	Zmax (cm)	Rmin (cm)	Rmax (cm)	SRLsee/seu (>20 MeV h.cm ⁻² /10 years)	RTCsee/seu (>20 MeV h.cm ⁻² /10 years)
760	770	80	90	1.52x10 ¹²	1.52 x10 ¹³

$$RTC_{see/seu} = SRL_{see/seu} * SF_{sim} * SF_{ldr} * SF_{lot}$$

SF _{sim} =	5	(Simulation)
SF _{ldr} =	1	(Low Dose Rate)
SF _{lot} =	2	(Lot Variation)

For production qualification the SF_{lot} factor decreases to 1 and hence the RTC decreases to 7.6 x10¹² (>20 MeV h.cm⁻²/10 years).

3. SEE/SEU Radiation Test Setup

A total of 4 test devices and 1 reference device was screened and used for the test. The test devices were obtained from a homogenous lot. The devices were biased under their normal operating condition. The four test devices were monitored for latchups due to see/seu effects during the irradiation period. The device parameters were measured prior to irradiation and upon receiving the devices back from the reactor.

The devices were irradiated using 226 MeV protons derived from the cyclotron at Northeast Proton Therapy center in Boston. Four of the devices were stacked and

irradiated together with less than 1% degradation in fluence from top of the stack to the bottom.

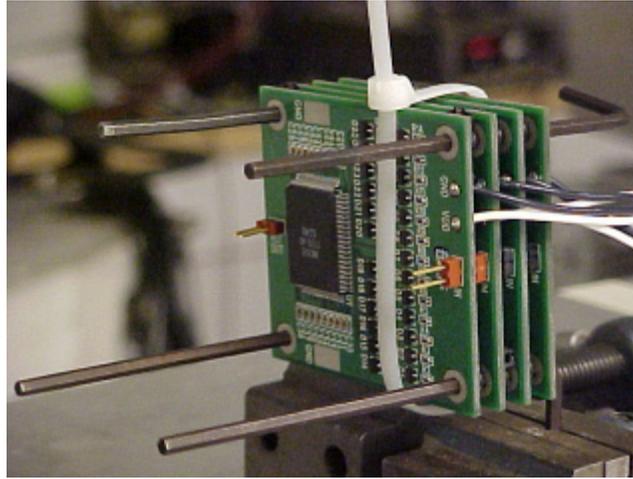


Figure 1. Preamp/Shapers Stack

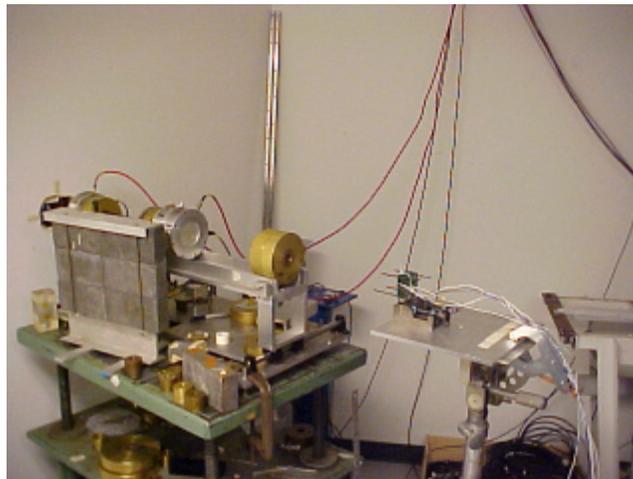


Figure 2. Preamp/Shaper Stack and the proton beam line

Electronics gain, shaping time, Equivalent Noise charge, input & output dc levels and current consumption of the devices were monitored and the rejection criteria for the devices were formulated as any failed channel or degradation of one or more parameters beyond the 10% level for charge gain, shaping time and 20% level for noise.

A total fluence of 2.6×10^{13} protons.cm⁻², which well above the RTC was delivered to the devices

4. Test Results

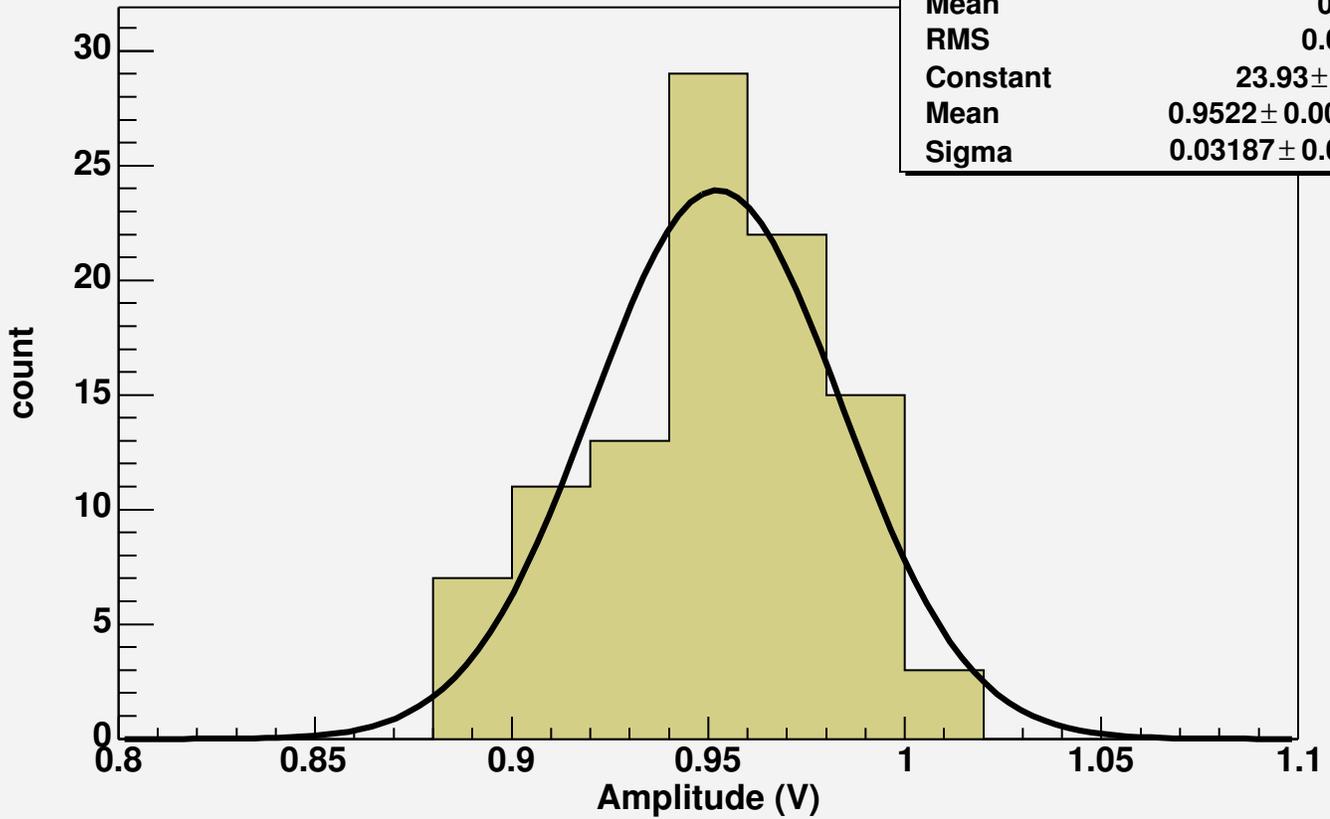
A total of 100 front-end channels were irradiated and none of them exhibited failures or exceeded the rejection criteria. The worst case degradation observed is outlined in the following table.

Parameter	% Change from Pre-Radiation Measurement
Charge Gain	0.5%
Shaping Time	0.8%
ENC	12.9%
Device Current	1.42%

5. Conclusion:

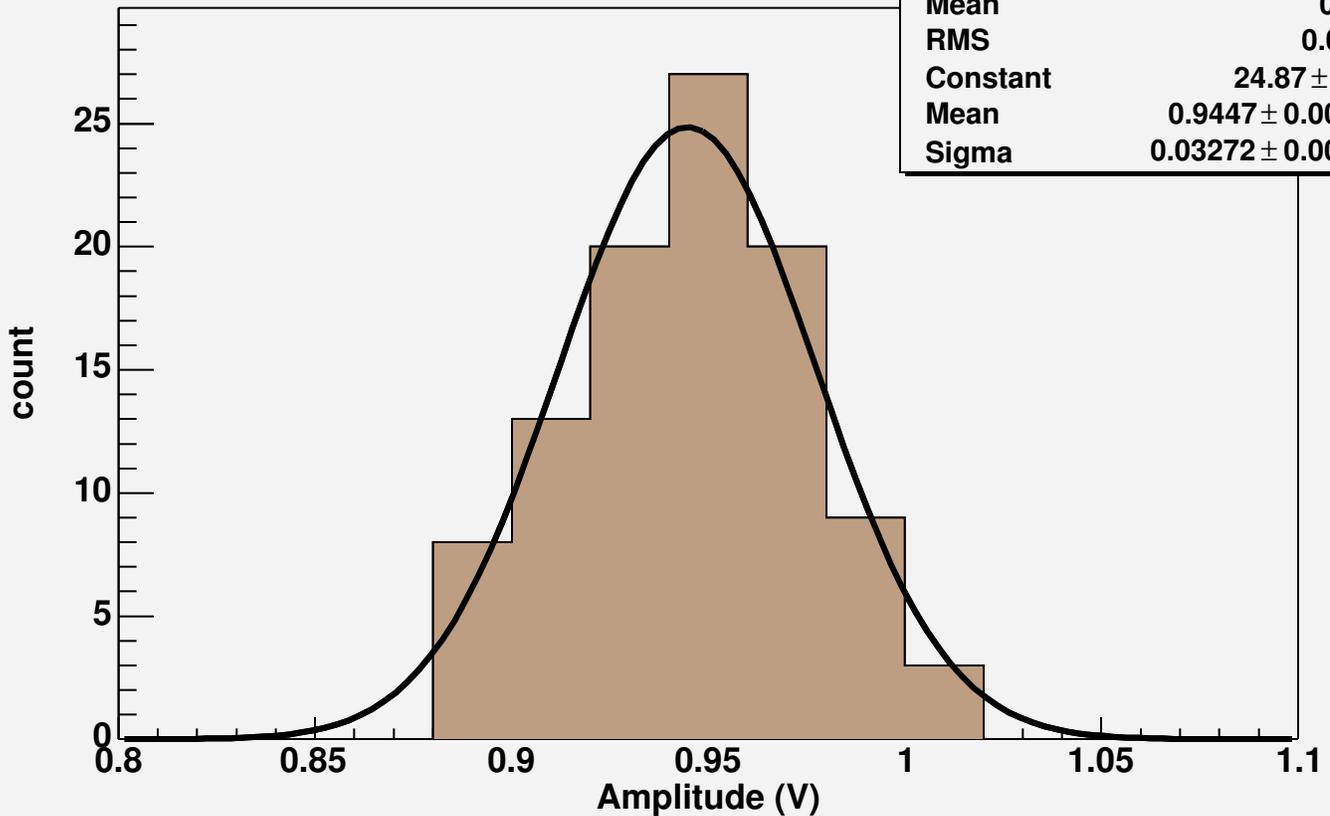
IC71 Preamp/Shaper was irradiated upto the Radiation tolerance criteria fluence for high energy hadrons. No single event effects or upsets were observed. The degradation of the front-end electronics module due to irradiation is well within the acceptable range for the CSC electronics requirement.

IC71 Amplitude



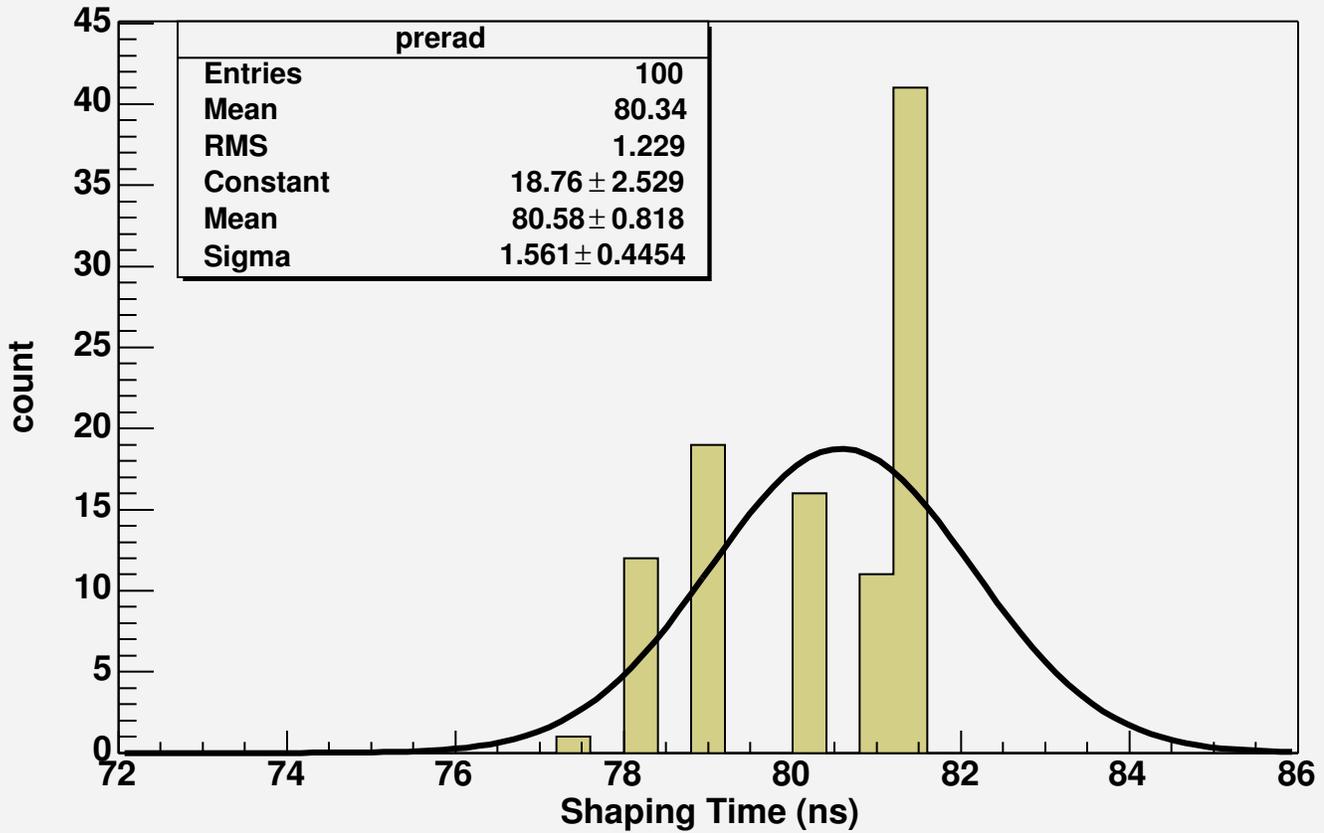
prerad	
Entries	100
Mean	0.9505
RMS	0.02974
Constant	23.93 ± 3.535
Mean	0.9522 ± 0.004166
Sigma	0.03187 ± 0.00395

IC71 Amplitude/MGH/226 MeV Proton beam

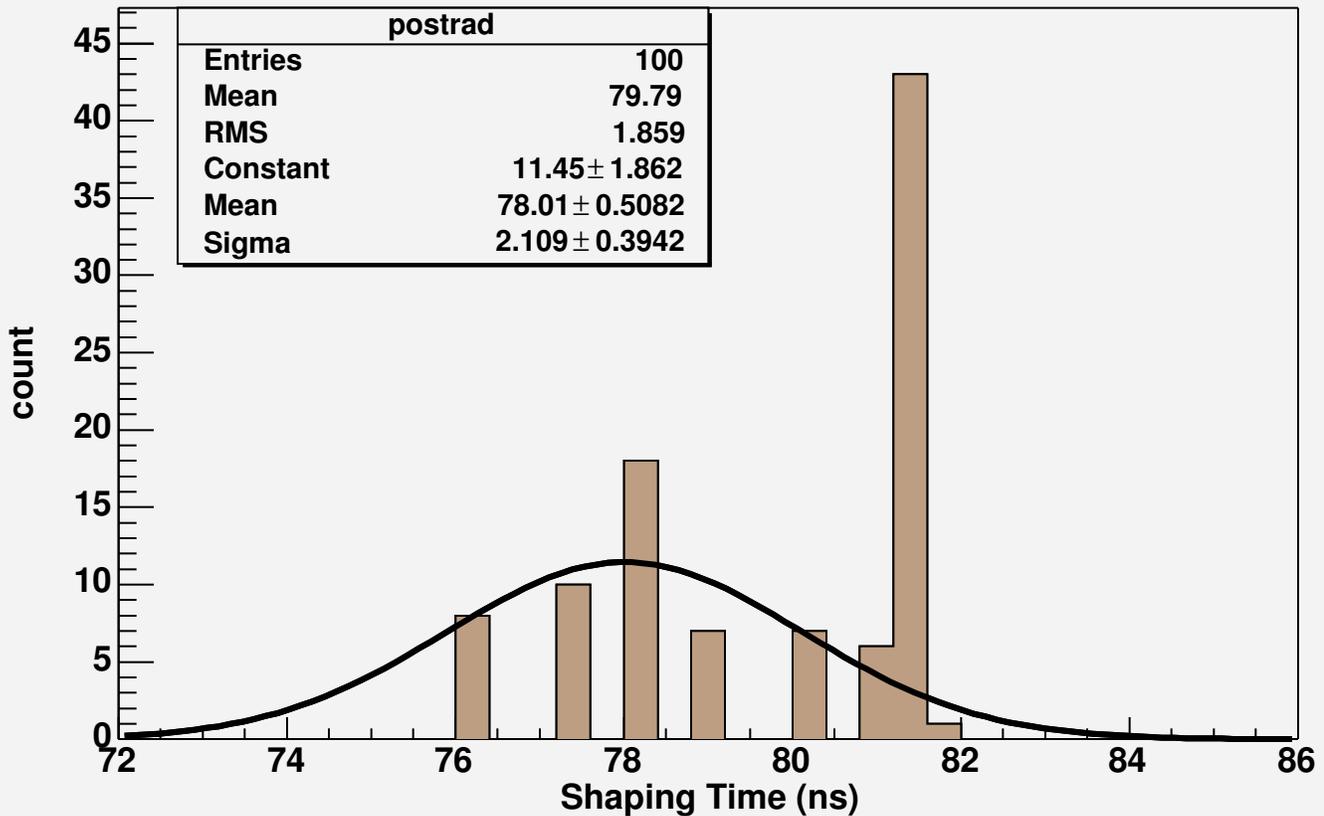


postrad	
Entries	100
Mean	0.9453
RMS	0.02915
Constant	24.87 ± 3.338
Mean	0.9447 ± 0.003862
Sigma	0.03272 ± 0.003579

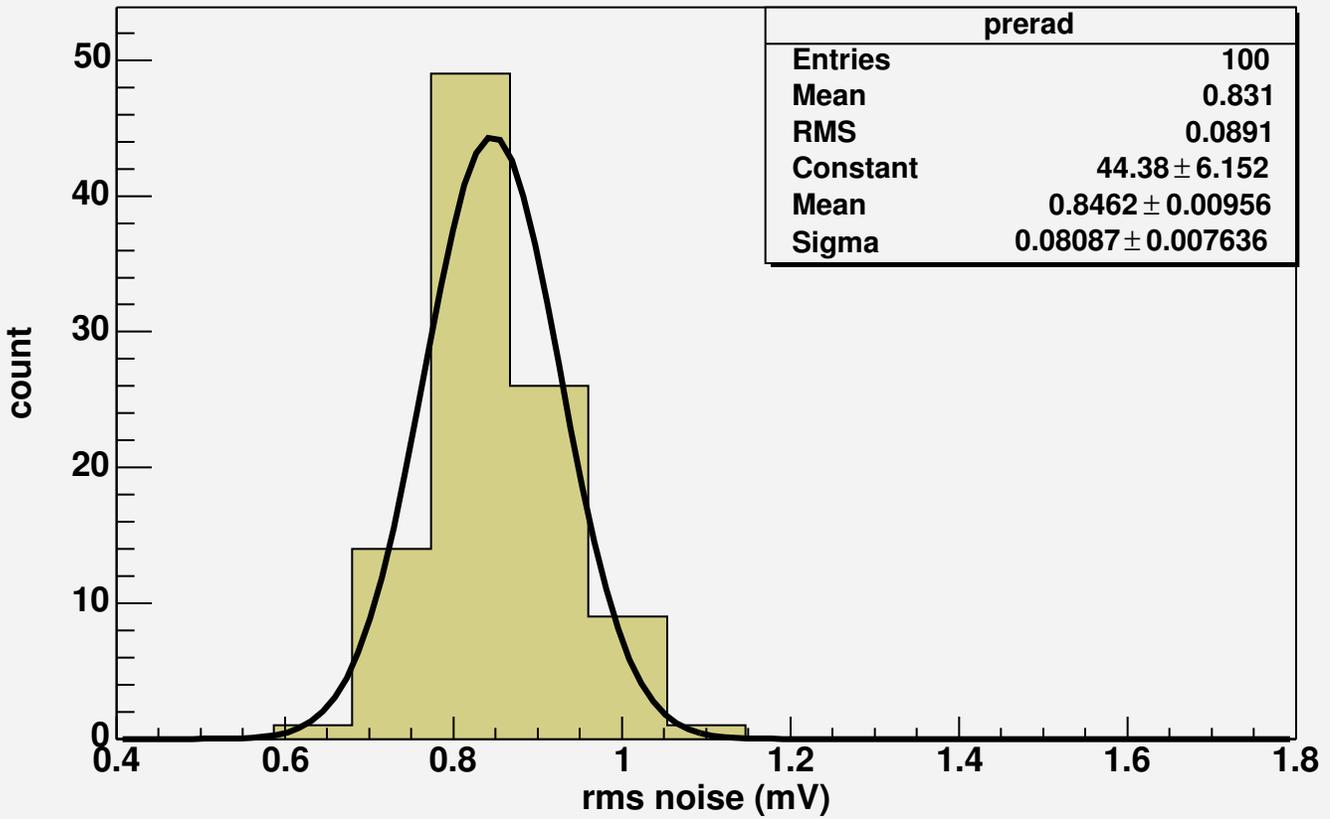
IC71 shaping



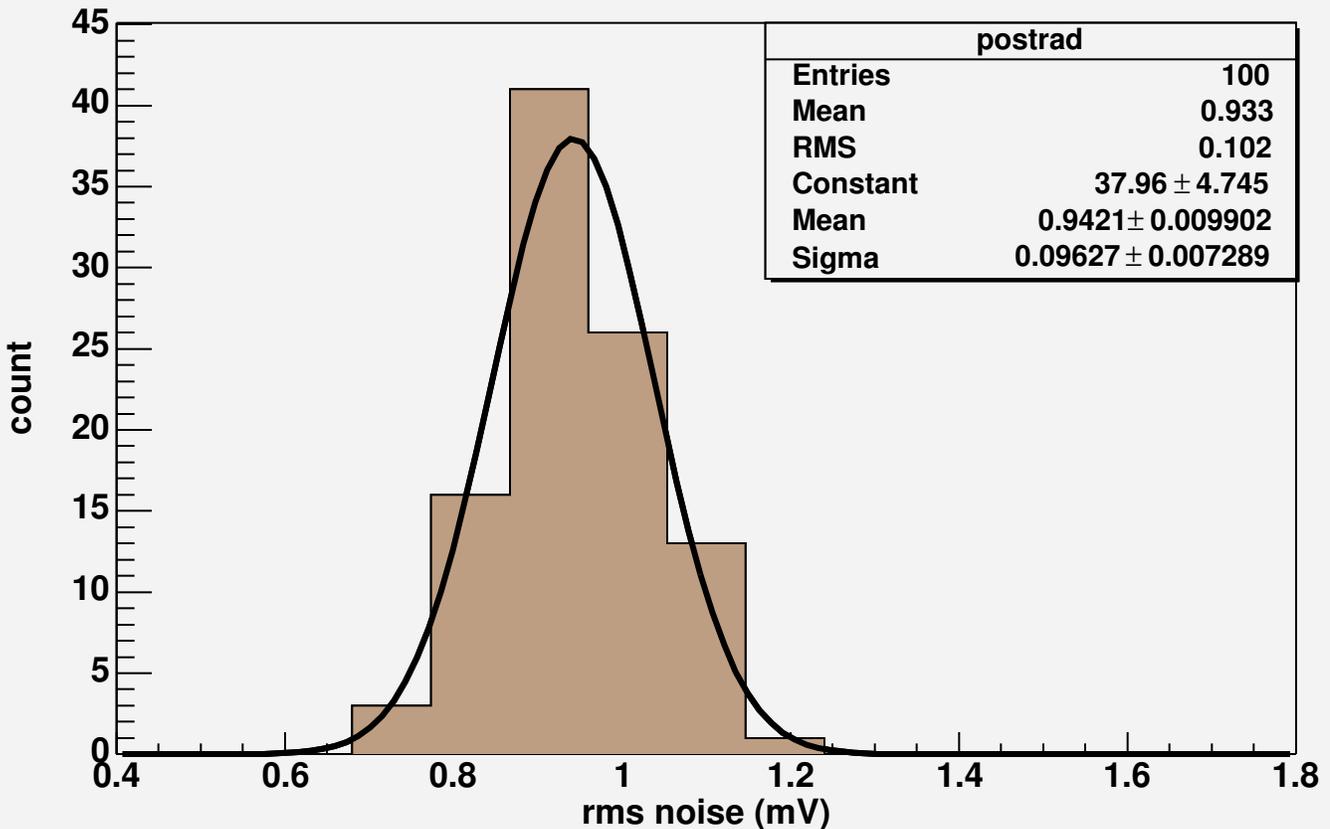
IC71 shaping/MGH/226 MeV Proton beam



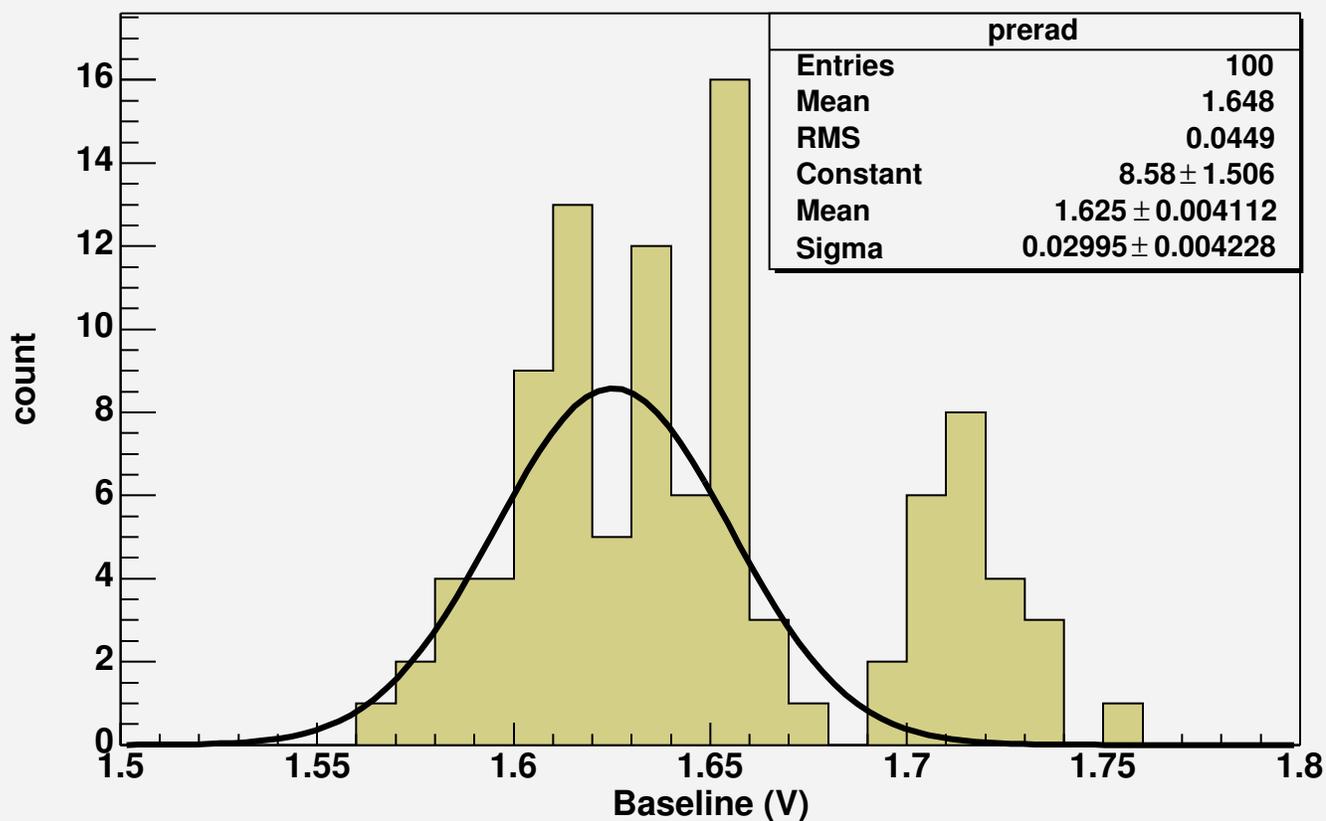
IC71 noise



IC71 noise/MGH/226 MeV Proton beam



IC71 Baseline



IC71 Baseline/MGH/226 MeV Proton beam

