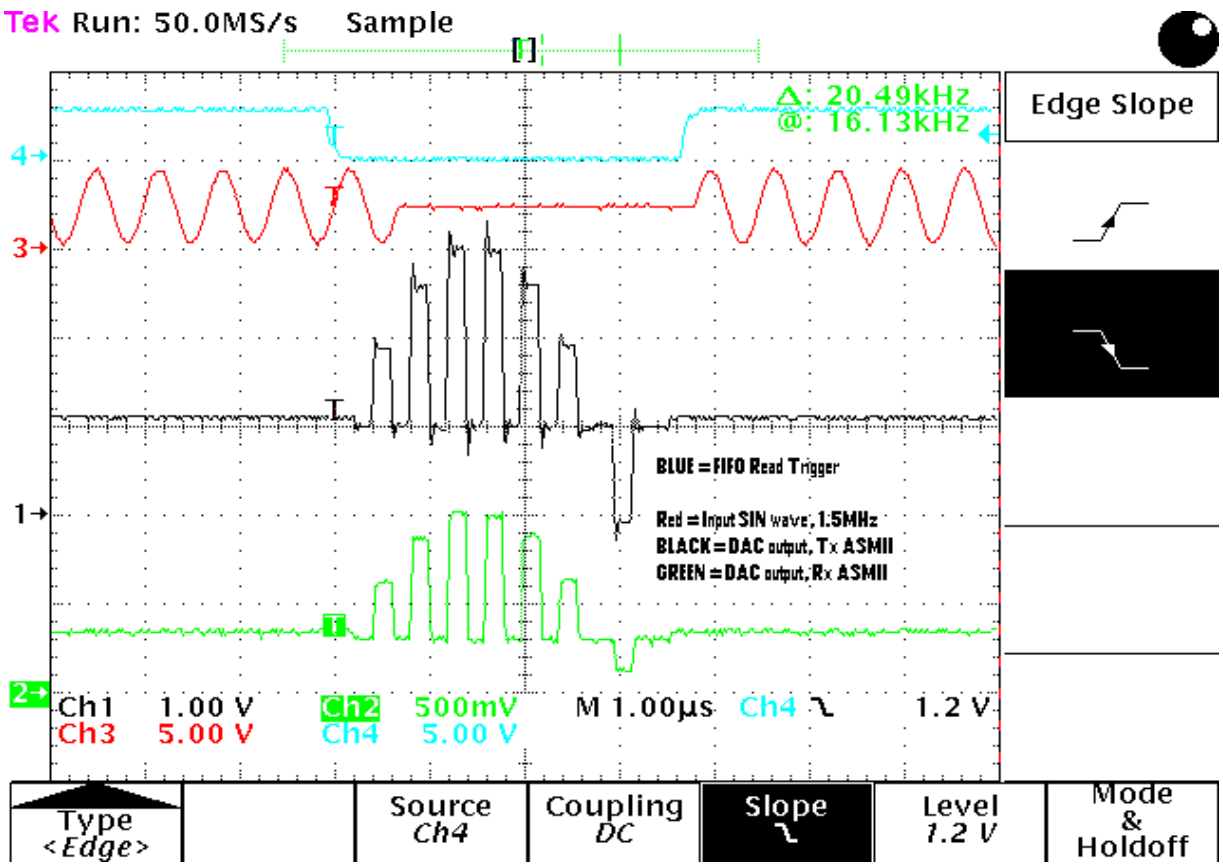
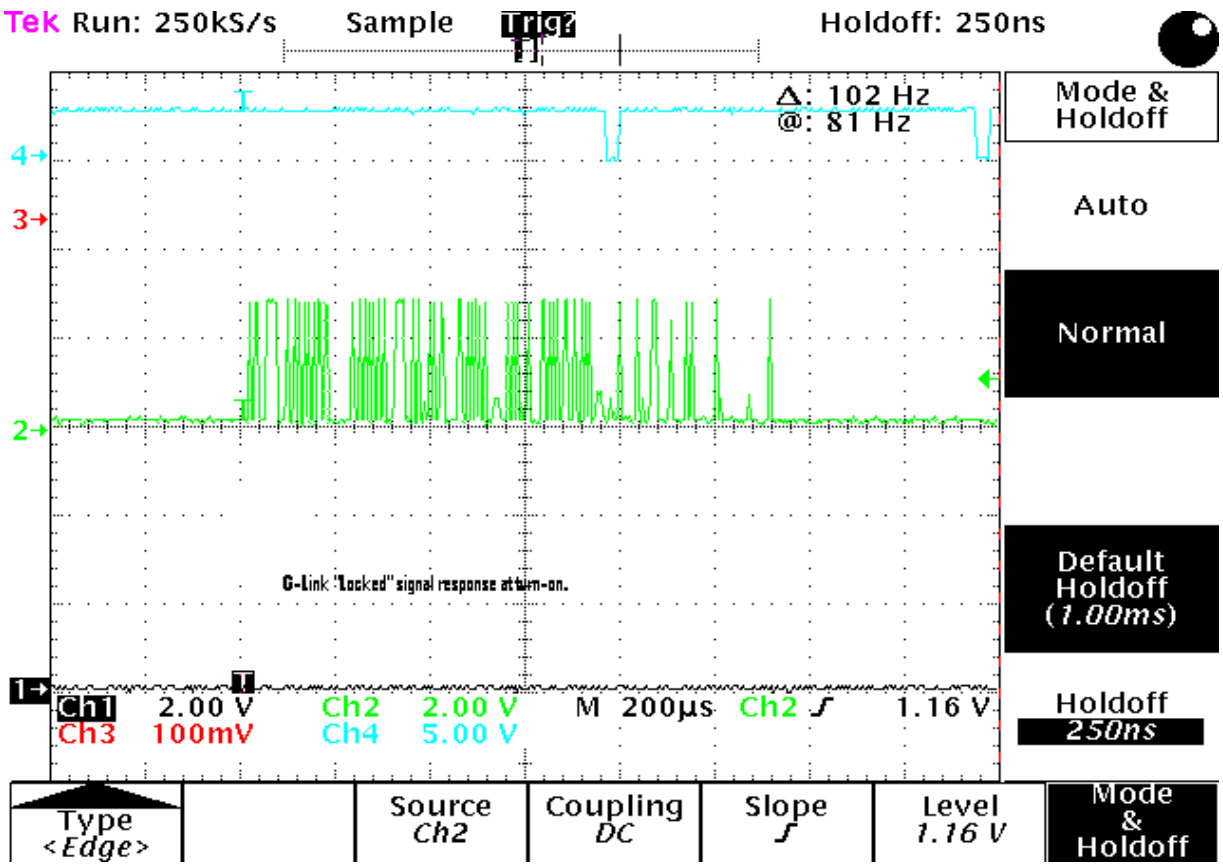
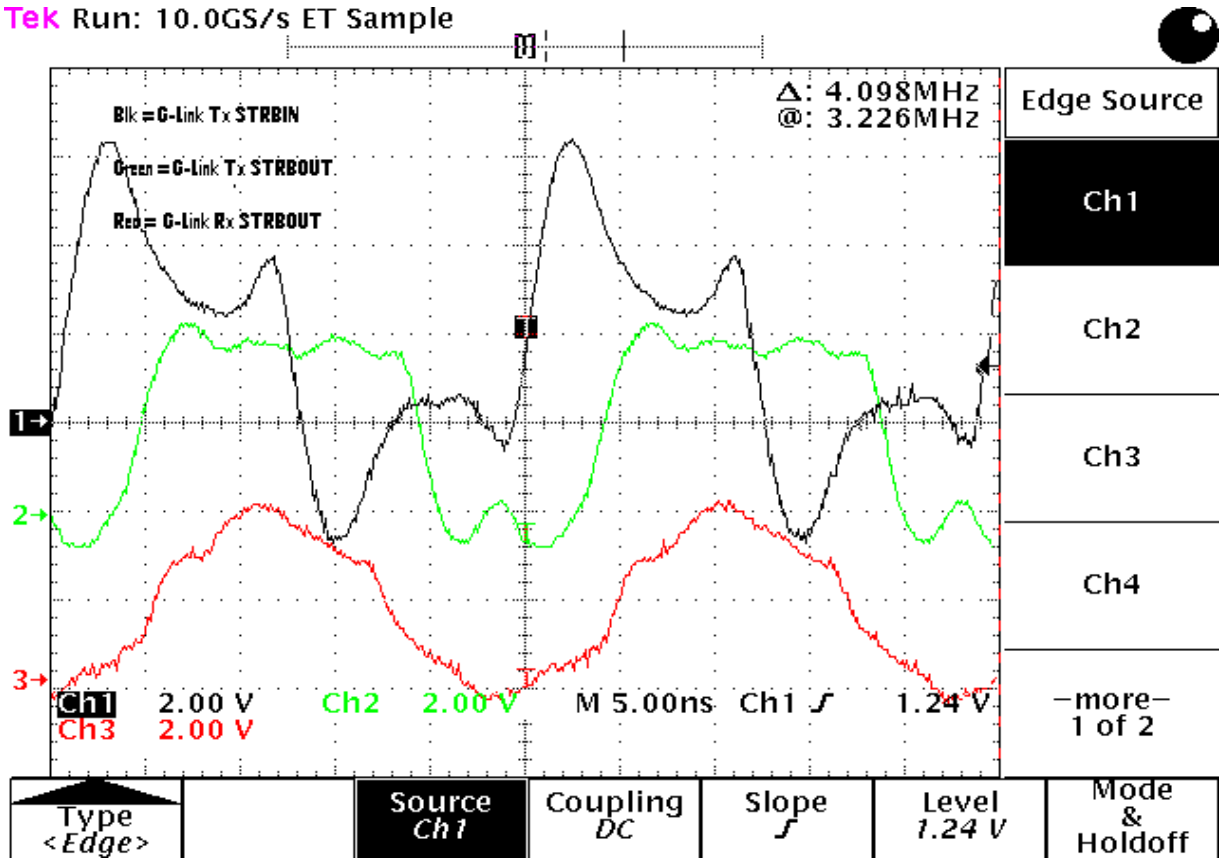
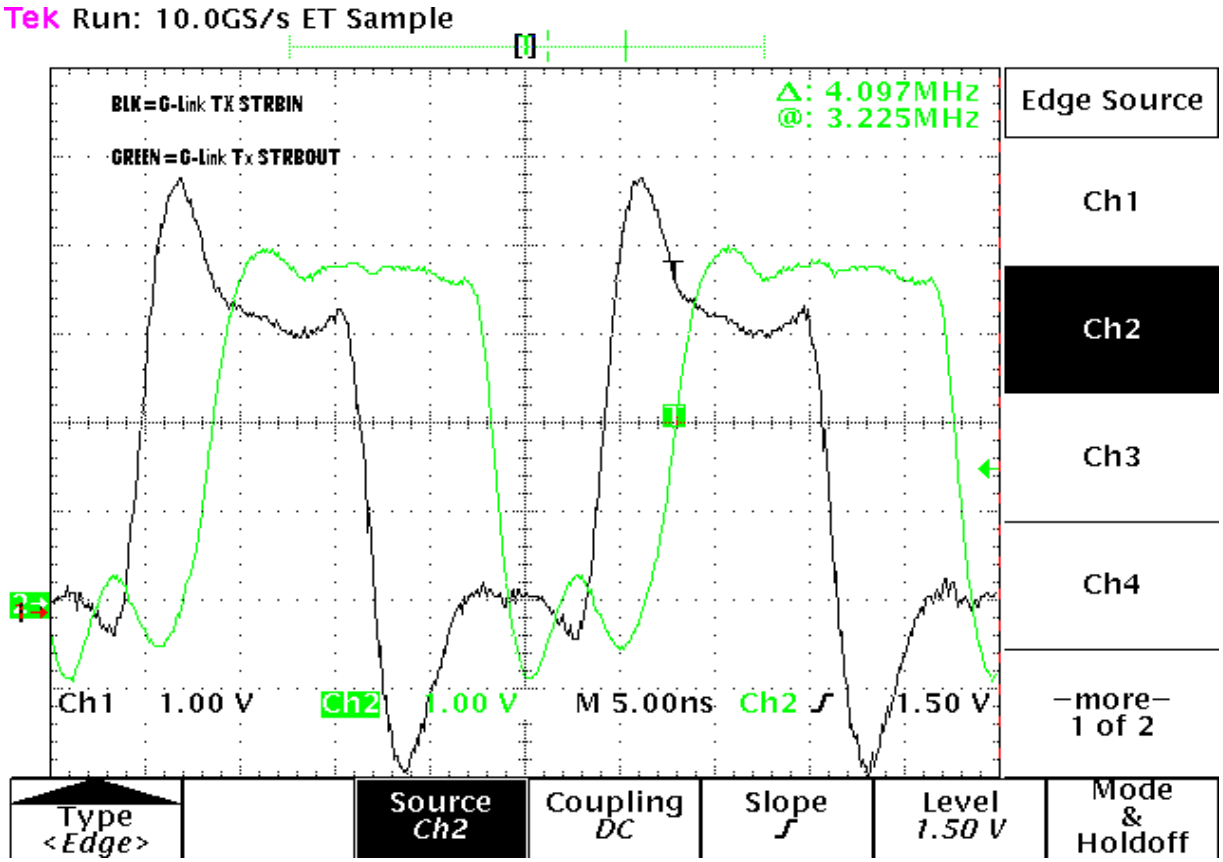

Tek Run: 50.0MS/s Sample
Δ: 20.49kHz
@: 16.13kHz
Edge Slope
BLUE = FIFO Read Trigger
Red = Input SIN wave, 1.5MHz
BLACK = DAC output, Tx ASMII
GREEN = DAC output, Rx ASMII
Ch1 1.00 V
Ch2 500mV
M 1.00µs Ch4
1.2 V
Ch3 5.00 V
Ch4 5.00 V
Type
<Edge>
Source
Ch4
Coupling
DC
Slope
Level
1.2 V
Mode
&
Holdoff


Tek Run: 250kS/s Sample Trig?
Holdoff: 250ns
Δ: 102 Hz
@: 81 Hz
Mode & Holdoff
Auto
Normal
Default Holdoff (1.00ms)
G-Link "Locked" signal response at turn-on.
Ch1 2.00 V Ch2 2.00 V M 200µs Ch2 1.16 V
Ch3 100mV Ch4 5.00 V
Holdoff 250ns
Type <Edge>
Source Ch2
Coupling DC
Slope
Level 1.16 V
Mode & Holdoff


Tek Run: 10.0GS/s ET Sample
Blk = G-Link Tx STRBIN
Green = G-Link Tx STRBOUT
Red = G-Link Rx STRBOUT
Δ: 4.098MHz
@: 3.226MHz
Ch1 2.00 V
Ch2 2.00 V
Ch3 2.00 V
M 5.00ns Ch1 ƒ 1.24 V
Edge Source
Ch1
Ch2
Ch3
Ch4
–more–
1 of 2
Type
<Edge>
Source
Ch1
Coupling
DC
Slope
ƒ
Level
1.24 V
Mode
&
Holdoff


Tek Run: 10.0GS/s ET Sample
BLK=G-Link TX STRBIN
GREEN=G-Link Tx STRBOUT
Δ: 4.097MHz
@: 3.225MHz
Ch1 1.00 V
Ch2 1.00 V
M 5.00ns Ch2 ƒ 1.50 V
Edge Source
Ch1
Ch2
Ch3
Ch4
–more–
1 of 2
Type
<Edge>
Source
Ch2
Coupling
DC
Slope
ƒ
Level
1.50 V
Mode
&
Holdoff