VME DAQ
REGISTER MAP

REGISTER NAME	BASE + (OFFSET)	BITS	DIR	FUNCTION
Reset	BASE+ 0x00	0..0	W	Board Reset (writing a 1 on bit zero will reset the board a read will return zero)
EXT_TRIG1	BASE+ 0x00	1..1	R	External Trigger 1 status
EXT_TRIG2	BASE+ 0x00	2..2	R	External Trigger 2 status
RST_ECNT	BASE+ 0x00	3..3	W	Will reset all event counters
F__EMPT1	BASE+ 0x00	4..4	R	FIFO 1 Empty Flag
F__EMPT2	BASE+ 0x00	5..5	R	FIFO 2 Empty Flag
F__EMPT3	BASE+ 0x00	6..6	R	FIFO 3 Empty Flag
F__EMPT4	BASE+ 0x00	7..7	R	FIFO 4 Empty Flag
B_ECNT	BASE_+ 0x00	8..15	R	Board event counter (same as FIFO 1)
F_FULL_1	BASE+ 0x02	0..0	R	FIFO 1 Full Flag
F_EMPT_1	BASE+ 0x02	1..1	R	FIFO 1 Empty Flag
F_AE_1	BASE+ 0x02	2..2	R	FIFO 1 Almost Empty Flag
F_AF_1	BASE+ 0x02	3..3	R	FIFO 1 Almost Full Flag
F_ECNT_1	BASE+ 0x02	8..15	R	FIFO 1 Event Counter
F_FULL_2	BASE+ 0x04	0..0	R	FIFO 2 Full Flag
F_EMPT_2	BASE+ 0x04	1..1	R	FIFO 2 Empty Flag
F_AE_2	BASE+ 0x04	2..2	R	FIFO 2 Almost Empty Flag
F_AF_2	BASE+ 0x04	3..3	R	FIFO 2 Almost Full Flag
F_ECNT_2	BASE+ 0x04	8..15	R	FIFO 2 Event Counter
F_FULL_3	BASE+ 0x06	0..0	R	FIFO 3 Full Flag
F_EMPT_3	BASE+ 0x06	1..1	R	FIFO 3 Empty Flag
F_AE_3	BASE+ 0x06	2..2	R	FIFO 3 Almost Empty Flag
F_AF_3	BASE+ 0x06	3..3	R	FIFO 3 Almost Full Flag

F_ECNT_3	BASE+ 0x06	8..15	R	FIFO 3 Event Counter
F_FULL_4	BASE+ 0x08	0..0	R	FIFO 4 Full Flag
F_EMPT_4	BASE+ 0x08	1..1	R	FIFO 4 Empty Flag
F_AE_4	BASE+ 0x08	2..2	R	FIFO 4 Almost Empty Flag
F_AF_4	BASE+ 0x08	3..3	R	FIFO 4 Almost Full Flag
F_ECNT_4	BASE+ 0x08	8..15	R	FIFO 4 Event Counter
ED_TX	BASE+ 0x0A	0..0	R/W	G-link TX control bit
CAV_TX	BASE+ 0x0A	1..1	R/W	G-link TX control bit
FLAG_TX	BASE+ 0x0A	2..2	R/W	G-link TX control bit
DAV_TX	BASE+ 0x0A	3..3	R/W	G-link TX control bit
SPARE1_t1	BASE+ 0x0A	4..4	R/W	G-link TX control bit
TX_LOCKED	BASE+ 0x0A	5..5	R	G-link TX control bit
TX_DATA	BASE+ 0x0C	0..15	R/W	GLINK TX DATA
FIFO 1 DATA	BASE+ 0x1000 - 0x1fff	0..15	R	FIFO 1 DATA
FIFO 2 DATA	BASE+ 0x2000 - 0x2fff	0..15	R	FIFO 2 DATA
FIFO 3 DATA	BASE+ 0x3000 - 0x3fff	0..15	R	FIFO 3 DATA
FIFO 4 DATA	BASE+ 0x4000 - 0x4fff	0..15	R	FIFO 4 DATA

Note:

- ?? 32 bit address space
- ?? Standard 16 bit Block transfers only